

# BHOJ REDDY ENGINEERING COLLEGE FOR WOMEN

Department Of Information Technology

Time Table for for the Academic Year 2015-16

II B.Tech I Semester IT Section-A

Room No:312 South Block (SB)

w.e.f: 01-07-2015

	0930-1030	1030-1130	1130-1230	1230-1330	1330-1430	1430-1530	1530-1630
	1	2	3		4	5	6
<b>Mon</b>	<b>BEE</b>	Tutorial DLDCO G1(312) DS- G2(306) BEE-G3(308)	<b>DS</b>	<b>L U N C H</b>	<b>DLDCO</b>	<b>EDC</b>	<b>MFCS</b>
<b>Tue</b>	<b>EDC</b>	<b>MFCS</b>	Tutorial BEE- G1(312) P&S- G2(306) EDC-G3(308)		<b>DS</b>	<b>P&amp;S</b>	<b>BEE</b>
<b>Wed</b>	←-----EE LAB-----→				Tutorial P&S-G1(312) DLDCO-G2(306) MFCS-G3(308)	<b>MFCS</b>	<b>DLDCO</b>
<b>Thu</b>	<b>DS</b>	<b>EDC</b>	<b>DLDCO</b>		<b>BEE</b>	Tutorial MFCS-G1(312) EDC-G2(306) DS-G3(308)	<b>P&amp;S</b>
<b>Fri</b>	<b>MFCS</b>	<b>P&amp;S</b>	Tutorial DS- G1(312) MFCS-G2(306) P&S-G3(308)		←-----DS LAB-----→		
<b>Sat</b>	<b>P&amp;S</b>	<b>BEE</b>	<b>DLDCO</b>		<b>DS</b>	Tutorial EDC-G1(312) BEE-G2(306) DLDCO-G3(308)	<b>EDC</b>

S.No	Subject	Name of the Faculty	Dept
1	Mathematical Foundations of Computer Science	G.Jyothi	IT
2	Digital Logical Design & Computer Organisation	D.Navaneetha	IT
3	Data Structures	M.Vinod	IT
4	Basic Electrical Enggineering	R.Manju Bhargavi	EEE
5	Electronic devices & Circuits	Neha Jain	ECE
6	Probability & Statistics	K.Padma	MATHS
7	Data structures Lab	M.Vinod,M.Srividya,D.Navaneetha	IT
8	Electrical and Electronics Lab	R.Manju Bhargavi,S.K.Vali/ Neha Jain Babu,G.Veena	EEE/ECE

Tutorial Groups(According to Serial Nos.)

G1: 1-20(312)

G2: 21-40(306)

G3: 41-60(308)

**Incharge**

**HOD**

**Principal**